

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L6	0	aloocat\$3 with stack with (etend\$3 or enlarg\$3 or expand\$3) with (memory or cache or buffer or register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/15 19:46
L7	9	allocat\$3 with stack with (etend\$3 or enlarg\$3 or expand\$3) with (memory or cache or buffer or register)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/15 19:47
L8	13	(increasing or expanding ir extending) with (address adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/15 19:50
L9	2357	711/211-220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 19:50
L10	1	L8 and L9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 19:50
L11	627	712/219,237,243.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:06
L12	1108	(extend\$3 or expand\$3 or enlarg\$3) with (memory or cache or buffer or register) with stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:07
L13	8469	711/?..ccls. or 711/2??..ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:08

L14	46	12 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:09
L15	2	11 and 12	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:09
S1	2	"20040172516"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:14
S2	7	tu-li-chun.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:36
S3	12	chen--ping-sheng.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:32
S4	6	S3 not S2	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:26
S5	4	tseng-pao-ching.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:33
S6	6	kuo-hung-cheng.in.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:34
S7	4733	8\$1bit near5 (micro\$1processor or micro\$1controller or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:38
S8	4606	stack adj pointer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 08:38

S9	4897	stack adj pointer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:38
S10	309	S7 and S9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:39
S11	87375	(increas\$3 or expand\$3 or enlarg\$3) near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:35
S12	757	(increas\$3 or expand\$3 or enlarg\$3) near5 internal near5 memory	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 08:40
S13	130	S10 and S11	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 10:26
S14	6	S10 and S12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:33
S15	2	adding near2 bits near5 (stack adj address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 10:02
S16	14	(stack adj pointer) near5 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 10:03
S17	2	S7 and S16	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 10:03
S18	85	mcs-31\$132 or mcs-51\$152	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:29

S19	691	711/220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
S20	4733	8\$1bit near5 (micro\$1processor or micro\$1controller or processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
S21	8	S20 and S19	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:30
S22	31008	(increas\$3 or expand\$3 or wid\$3) near5 address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:36
S23	420	S20 and S22	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:35
S24	4476	(increas\$3 or expand\$3 or wid\$3) adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:36
S25	118	S20 and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 13:37
S26	4	S25 and ("8051" or mcs-51\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:42
S27	5	(extend\$3 or exapanding) near5 (address adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:57
S28	61	configur\$3 with (address adj (width or size))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:53

S29	1	S20 and S28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:53
S30	7521	(extend\$3 or exapad\$3) near5 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:58
S31	4055	(extend\$3 or exapad\$3) near2 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:01
S32	133	S20 and S31	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 13:58
S33	1040	(extending or exapading) near2 (address)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:02
S34	15	S20 and S33	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:07
S35	0	"adding bits to address"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08
S36	0	adding adj bits adj to adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08
S37	1	adding adj bits adj address	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 14:08
S38	127	address near5 extender	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:34

S39	3	S20 and S38	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:33
S40	28	address adj extender	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 14:47
S41	39	S20 and "adding bits"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:15
S42	829	concatenat\$3 near5 address near2 bits	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:16
S43	16	S20 and S42	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:23
S44	39	S20 and (adding adj bits)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:29
S45	13	(increasing or expanding ir extending) with (address adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/07/23 15:30
S46	2273	711/211-220.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/07/23 16:15
S47	1	S45 and S46	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 19:50
S48	6594	stack near5 pointer	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/12/15 12:24

S49	2	"6788592"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 14:54
S50	58	extended near5 memory with stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 15:54
S51	310	711/2.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 15:55
S52	45	S51 and stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 15:59
S53	302	(extend\$3 or expand\$3) near5 (memory or cache or buffer or register) near5 stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:13
S54	8	S51 and S53	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:13
S55	6223	711/2???.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:13
S56	1044	(extend\$3 or expand\$3) with (memory or cache or buffer or register) with stack	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 20:07

S57	27825	"711"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:15
S59	7228	S55 or S56	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:15
S60	3093	"711"/?.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:16
S61	8469	S55 or S60	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 16:16
S62	46	S56 and S61	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/12/15 19:45